

N Channel MOSFET (Deep Trench Superjunction)



Lead Free Package and Finish

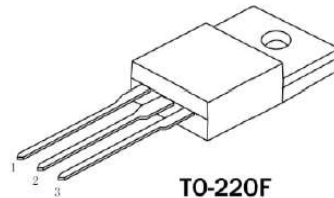
Applications:

- Adapter & Charger
- SMPS Standby Power
- AC-DC Switching Power Supply
- LED driving power

I_D	$R_{DS(ON)}$ (Typ.)	V_{DSS}
4.0A	0.84 Ω	600V

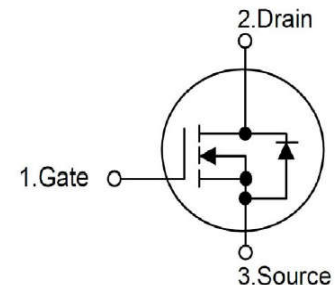
Features:

- Low On Resistance
- Low Gate Charge
- Peak Current vs Pulse Width Curve
- Proprietary New Super Junction Technology
- RoHS Compliant



TO-220F

Not to Scale



Ordering Information

Part Number	Package	Marking
RSU4N60F	TO-220F	RSU4N60F

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise specified

Symbol	Parameter	RSU4N60F	Units
V_{DSS}	Drain-to-Source Voltage (Note*1)	600	V
I_D	Continuous Drain Current	4.0	A
I_{DM}	Pulsed Drain Current (Note*2)	12.0	
PD	Power Dissipation	32	W
VGS	Gate-to-Source Voltage	± 30	V
EAS	Single Pulse Avalanche Energy L=10mH $V_{DD}=50V$ $T_J=25^\circ\text{C}$	20	mJ
TL TPKG	Maximum Temperature for Soldering	300 260	$^\circ\text{C}$
	Leads at 0.063in(1.6mm)from Case for 10 seconds		
	Package Body for 10 seconds		
T_J and T_{STG}	Operating Junction and Storage Temperature Range	-55 to 150	

*Drain Current Limited by Maximum Junction Temperature

Caution:Stresses greater than those listed in the “Absolute Maximum Ratings” Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RSU4N60F	Units	Test Conditions
$R_{\theta JC}$	Junction-to-Case	3.9	$^\circ\text{C}/\text{W}$	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of $+150^\circ\text{C}$.
$R_{\theta JA}$	Junction-to-Ambient	100		1 cubic foot chamber, free air.

OFF Characteristics $T_J=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BV _{DSS}	Drain-to-source Breakdown Voltage	600	---	---	V	V _{GS} =0V, I _D =250 μ A
I _{DSS}	Drain-to-Source Leakage Current	---	---	1.0	μ A	V _{DS} =600V, V _{GS} =0V
I _{GSS}	Gate-to-Source Forward Leakage	---	---	100	nA	V _{GS} =+30V V _{DS} =0V
	Gate-to-Source Reverse Leakage	---	---	-100		V _{GS} =-30V V _{DS} =0V

ON Characteristics $T_J=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{DS(on)}	Static Drain-to-Source On-Resistance	---	0.84	0.90	Ω	V _{GS} =10V, I _D =2A
V _{GS(TH)}	Gate Threshold Voltage	2.0	---	4.0	V	V _{GS} =V _{DS} , I _D =250 μ A

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t _{d(ON)}	Turn-on Delay Time	---	8.0	---	nS	V _{DS} =480V I _D =4.0A R _G =25 Ω (Note:3, 4)
t _{rise}	Rise Time	---	6.0	---		
t _{d(OFF)}	Turn-OFF Delay Time	---	35	---		
t _{fall}	Fall Time	---	20	---		

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
C _{iss}	Input Capacitance	---	350	---	pF	V _{GS} =0V V _{DS} =50V f=1.0MHz
C _{oss}	Output Capacitance	---	40	---		
C _{rss}	Reverse Transfer Capacitance	---	4	---		
Q _g	Total Gate Charge	---	7	---	nC	V _{DS} =480V I _D =4.0A V _{GS} =10V (Note:3, 4)
Q _{gs}	Gate-to-Source Charge	---	1.5	---		
Q _{gd}	Gate-to-Drain("Miller") Charge	---	2.5	---		

Source-Drain Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I _S	Continuous Source Current	--	--	4.0	A	Integral pn-diode in MOSFET
I _{SM}	Maximum Pulsed Current	--	--	12.0	A	
V _{SD}	Diode Forward Voltage	--	0.9	1.2	V	I _S =4.0A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	--	200	--	nS	V _{GS} =0V V _R =480V I _S =4.0A, di/dt=100A/μs
Q _{rr}	Reverse Recovery Charge	--	0.8	--	μC	

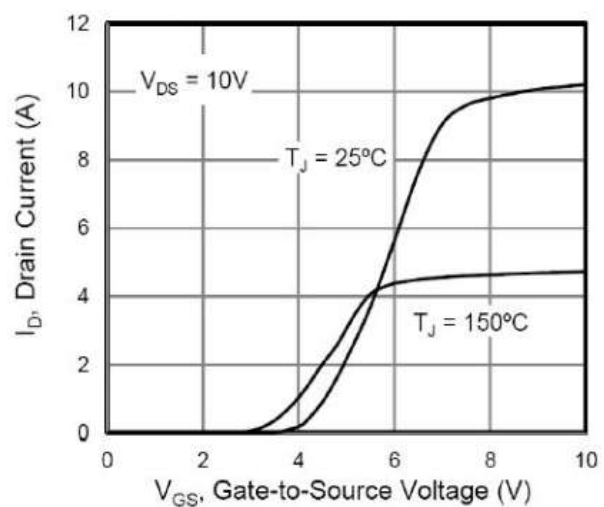
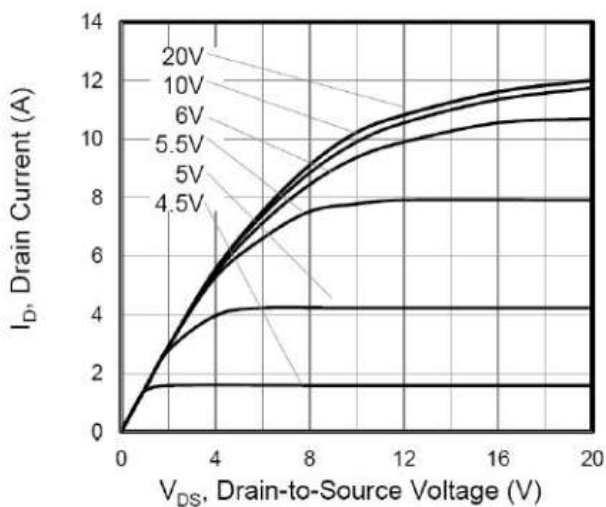
Notes:

- *1. T_J=±25°C to +150°C.
- *2. Repetitive rating; pulse width limited by maximum junction temperature.
- *3. Pulse width ≤ 380μs; duty cycle ≤ 2%.
- *4. Basically not affected by temperature.

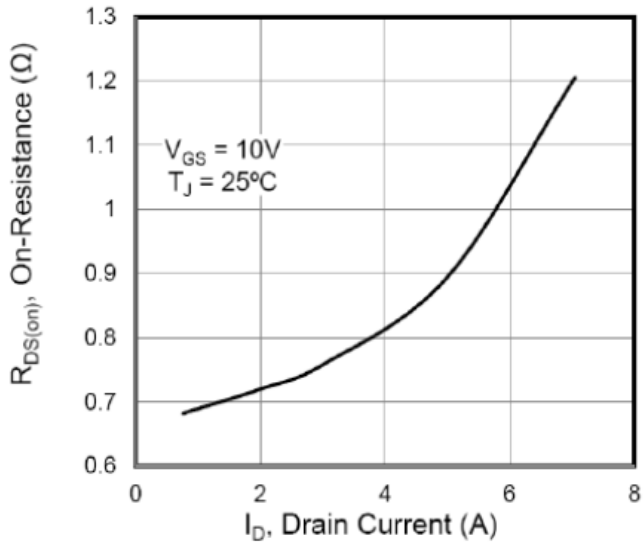
Typical Feature curve

Figure1. Typical Output Characteristics

Figure2. Typical Transfer Characteristics



Figuer3. Typical ON Resistance vs Drain Current



Figuer4. Typical Body Diode Transfer Characteristics

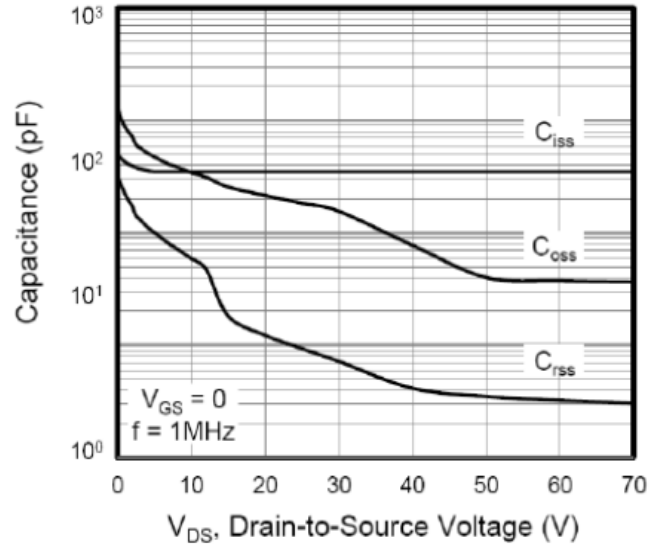


Figure5. Body Diode Forward Voltage

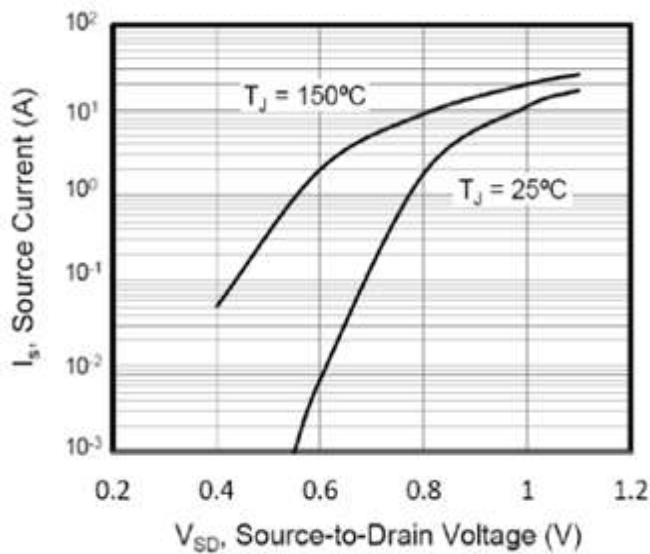


Figure6. Typical Gate Charge vs Gate-to-Source Voltage

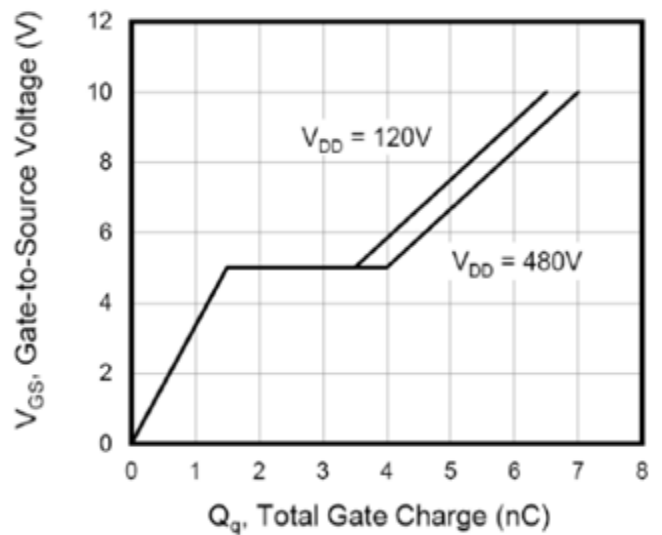


Figure7. Threshold Voltage vs Junction Temperature

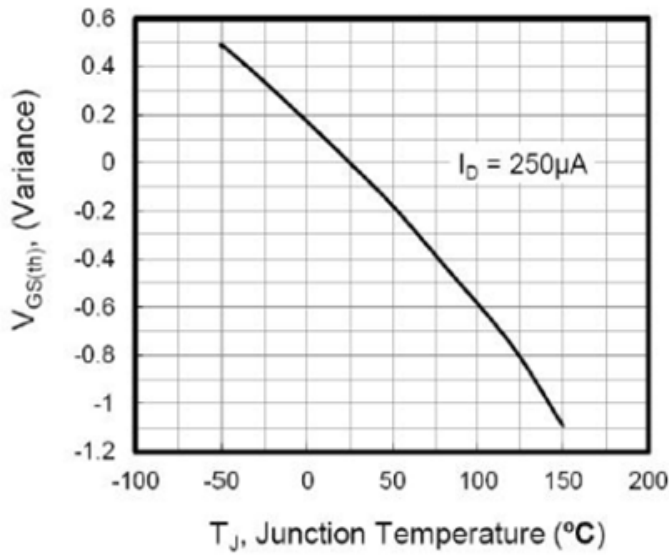
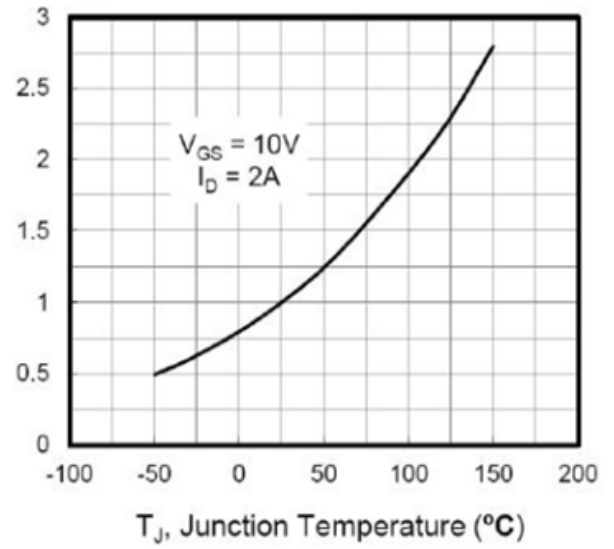
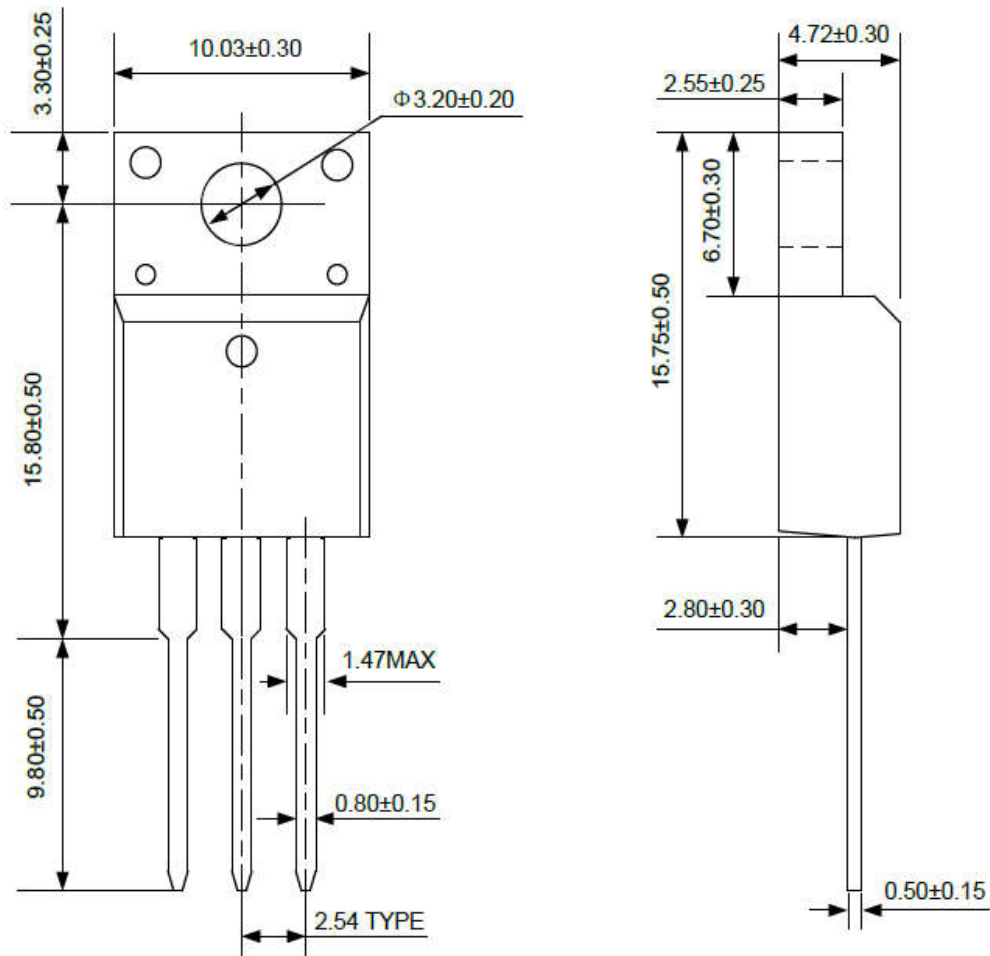


Figure8. Typical Drain-to-Source ON Resistance vs Junction Temperature



Package outline drawing



TO-220F

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